

**AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims**

**Claim 1 (Original):** A modulator comprising:

a PLL including a voltage-controlled oscillator, a frequency divider and a phase comparator,

said modulator generating a modulating signal based on the modulation data having information on a wider bandwidth than the frequency bandwidth of the PLL, setting the division ratio of said frequency divider by way of this modulating signal, and outputting a modulated carrier signal from said voltage-controlled oscillator as well as performing filtering processing by way of a pre-distortion filter on said modulation data to provide a frequency characteristic inverse to the frequency characteristic of said PLL in a process of generating said modulation signal, thereby allowing wideband modulation,

wherein said modulator comprises:

error detection means for detecting a difference between an amplitude value in a frequency equal to or below the cutoff frequency of said PLL and an amplitude value in a frequency higher than said cutoff frequency concerning an ac component of said modulating signal appearing on a control terminal of said voltage-controlled oscillator, and

frequency characteristic correction means for correcting at least one of the frequency characteristic of said PLL and the frequency characteristic of said pre-distortion filter in a direction said detected difference is eliminated.

**Claim 2 (Original):** The modulator according to claim 1, said modulator comprising a selector for selectively inputting, as said modulation data, first calibration data on a frequency equal to or below the cutoff frequency of said PLL and second calibration data on a frequency above the cutoff frequency of said PLL.

**Claim 3 (Original):** A modulator comprising:  
a PLL including,  
a voltage-controlled oscillator for outputting a modulated carrier signal,  
a frequency divider for dividing the frequency of an output signal of said voltage controlled oscillator by a modulated division ration,  
a phase comparator for comparing the phase of the output signal of said frequency divider and the phase of a reference signal and outputting the phase difference,  
a charge pump for converting the output signal of said phase comparator to a voltage or a current, and

a loop filter for performing low-pass filtering of the output signal of said charge pump and outputting the resulting signal to said voltage-controlled oscillator;

a modulation data generator for generating and outputting modulation data having the information on a wider bandwidth than the bandwidth of said PLL;

a pre-distortion filter which has a characteristic inverse to the frequency characteristic of said PLL approximated and which filters said modulation data;

division ratio modulation means for modulating the output signal of said pre-distortion filter and outputting the resulting output signal as a modulating signal used to set the division ratio of said frequency divider; and

a pre-distortion filter frequency characteristic correction means for outputting a control signal for varying the frequency characteristic of said pre-distortion filter.

**Claim 4 (Original):** The modulator according to claim 3, said modulator comprising:

a calibration data generator for generating and outputting, to said pre-distortion filter, first calibration data having the frequency information on the frequency band of said PLL and

second calibration data having the frequency information on the band outside the frequency band of said PLL,

wherein said pre-distortion filter frequency characteristic correction means comprises:

an A/D converter for converting to a digital signal the amplitude value of an ac component having the division ratio modulated by said division ratio modulation means appearing on the output of said loop filter in response to said first and second calibration data respectively;

comparison means for comparing the data of said two amplitude values output from said A/D converter and outputting the difference information; and

filter characteristic control means for varying the characteristic of said pre-distortion filter in accordance with said difference information output from said comparison means.

**Claim 5 (Original):** The modulator according to claim 3, said modulator comprising:

a calibration data generator for generating and outputting, to said pre-distortion filter, first calibration data having the frequency information on the frequency band of said PLL and second calibration data having the frequency information on the band outside the frequency band of said PLL and

a demodulator for demodulating the output signal of said voltage-controlled oscillator,

wherein said pre-distortion filter frequency characteristic correction means comprises:

an A/D converter for converting to a digital signal the amplitude value of an ac component having the division ratio modulated by said division ratio modulation means appearing on the output of said loop filter in response to said first and second calibration data respectively;

comparison means for comparing for comparing the data of said two amplitude values output from said A/D converter and outputting the difference information; and

filter characteristic control means for varying the characteristic of said pre-distortion filter in accordance with said difference information output from said comparison means.

**Claim 6 (Original):** A modulator comprising:

a PLL including,

a voltage-controlled oscillator for outputting a modulated carrier signal,

a phase comparator for comparing the phase of the output signal of said frequency divider and the phase of a reference signal and outputting the phase difference,

a charge pump for converting the output signal of said phase comparator to a voltage or a current, and

a loop filter for performing low-pass filtering of the output signal of said charge pump and outputting the resulting signal to said voltage-controlled oscillator;

a modulation data generator for generating and outputting modulation data having the information on a wider bandwidth than the bandwidth of said PLL;

a pre-distortion filter which has a characteristic inverse to the frequency characteristic of said PLL approximated and which filters said modulation data; division ratio modulation means for modulating the output signal of said pre-distortion filter and outputting the resulting output signal as a modulating signal used to set the division ratio of said frequency divider; and

a PLL frequency characteristic correction means for outputting a control signal for varying the current gain of said charge pump.

**Claim 7 (Original):** The modulator according to claim 6, said modulator comprising:

a calibration data generator for generating and outputting, to said pre-distortion filter, first calibration data having the frequency information on the frequency band of said PLL and second calibration data having the frequency information on the band outside the frequency band of said PLL,

wherein said PLL frequency characteristic correction means comprises:

an A/D converter for converting to a digital signal the amplitude value of an ac component having the division ratio modulated by said division ratio modulation means appearing on the output of said loop filter in response to said first and second calibration data respectively;

comparison means for comparing the data of said two amplitude values output from said A/D converter and outputting the difference information; and

charge pump current control means for varying the current gain of said charge pump in accordance with said difference information output from said comparison means.

**Claim 8 (Original):** The modulator according to claim 6, said modulator comprising:

a calibration data generator for generating and outputting, to said pre-distortion filter, first calibration data having the frequency information on the frequency band of said PLL and second calibration data having the frequency information on the band outside the frequency band of said PLL and

a demodulator for demodulating the output signal of said voltage-controlled oscillator,

wherein said PLL frequency characteristic correction means comprises:

an A/D converter for converting to a digital signal the amplitude value of an ac component having the division ratio modulated by said division ratio modulation means appearing on the output of said loop filter in response to said first and second calibration data respectively;

comparison means for comparing the data of said two amplitude values output from said A/D converter and outputting the difference information; and

charge pump current control means for varying the current gain of said charge pump in accordance with said difference information output from said comparison means.

**Claim 9 (Currently amended):** The modulator according to any one of claims claim 3 through 5, wherein said filter characteristic control means comprises a memory for storing control data used to change the frequency characteristic of said pre-distortion filter.

**Claim 10 (Currently amended):** The modulator according to any one of claims claim 6 through 8, wherein said charge pump current control means comprises a memory for storing control data used to change the frequency characteristic of said PLL.

**Claim 11 (Currently amended) :** The modulator according to ~~any one of claims 3 through 10~~claim 3 or 6, wherein a low-pass filter is provided having a higher cutoff frequency than the bandwidth of said modulating signal between the output end of said loop filter and the input end of said voltage-controlled oscillator.

**Claim 12 (Currently amended) :** The modulator according to any one of claims 4, 5, 7 and ~~or~~ 8 through 11, wherein said first and second calibration data have a single frequency information item.

**Claim 13 (Currently amended) :** The modulator according to ~~any one of claims~~ claim 4[1,1] or 5 and 9, wherein, in said pre-distortion filter frequency characteristic correction means, said comparison means compares the amplitude values of an ac component having the division ratio modulated by said division ratio modulation means appearing on the output of said loop filter in response to said first and second calibration data respectively, immediately after varying the output frequency of said voltage-controlled oscillator, and said filter characteristic control means varies the characteristic of said pre-distortion filter in accordance with said comparison result.

**Claim 14 (Original):** The modulator according to claim 4 or 7, wherein said loop filter and said A/D converter are ac-coupled with each other.

**Claim 15 (Original):** The modulator according to claim 4 or 5, wherein said modulator halts the operation of said A/D converter after varying the characteristic of said pre-distortion filter.

**Claim 16 (Original):** The modulator according to claim 5, wherein said modulator halts the operation of said demodulator after varying the characteristic of said pre-distortion filter.

**Claim 17 (Currently amended):** The modulator according to ~~any one of claims 3 through 10~~claim 3 or 6, wherein said pre-distortion filter is an IIR-type digital filter.

**Claim 18 (Currently amended):** A mobile radio unit comprising the modulator according to ~~any one of claims 1 through 17~~claim 1, 3 or 6.

**Claim 19 (Currently amended):** A radio base station comprising the modulator according to ~~any one of claims 1 through 17~~ claim 1, 3 or 6.

**Claim 20 (Original):** A method for correcting a modulator which generates a modulating signal based on the modulation data having information on a wider bandwidth than the frequency bandwidth of the PLL, sets the division ratio of said frequency divider by way of this modulating signal, and outputs a modulated carrier signal from said voltage-controlled oscillator as well as performs filtering processing by way of a pre-distortion filter on said modulation data to provide a frequency characteristic inverse to the frequency characteristic of said PLL in a process of generating said modulation signal, thereby allowing wideband modulation, wherein said method comprises an error detecting step of detecting a difference between an amplitude value in a frequency equal to or below the cutoff frequency of said PLL and an amplitude value in a frequency higher than said cutoff frequency concerning an ac component of said modulating signal appearing on a control terminal of said voltage-controlled oscillator, and a frequency characteristic correcting step of correcting at least one of the frequency characteristic of said PLL and the frequency characteristic of said pre-distortion filter in a direction said detected difference is eliminated.